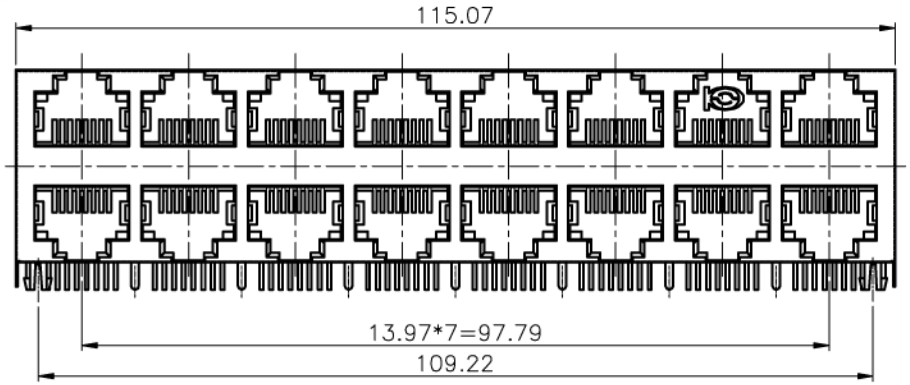
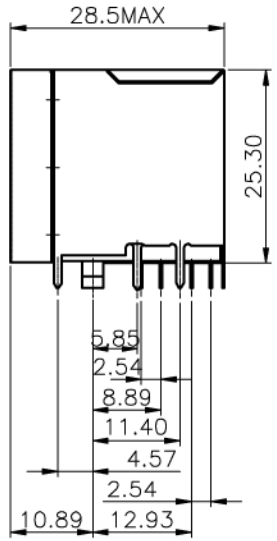
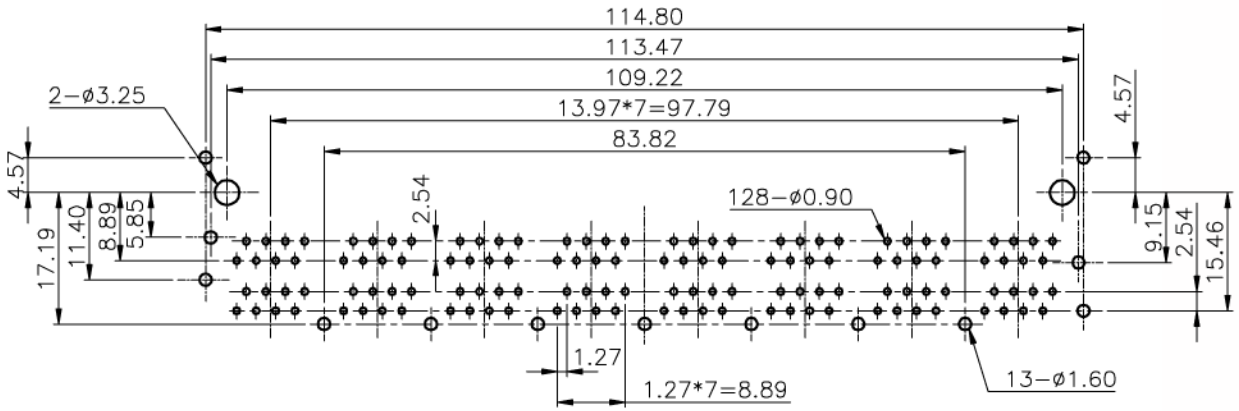




PCB LAYOUT/TOP VIEW



修正 REVISION	日期 DATE	容許公差 TOLERANCE		ARTICLE	Multi-Port PCB Jacks		
		.XXX	±0.10				
		.XX	±0.25				
		.X	±0.38				
		X	±0.50	DWG NO.	302208S		
		APPD.					
Bomar Interconnect		CHKD.		UNIT	SCALE	SHEET	REV.
		DR.		mm	2/1	1/1	A