



修正	REVISION	日期	DATE	设计公差	TOLERANCE
				.XXX	±0.10
				.XX	±0.25
				.X	±0.38
				X	±0.50
		APPL		DWG NO.	302204S
		CHKD		UNIT	mm
		DR.		SCALE	2/1
				SHEET	1/1
				REV.	A

Bomar Interconnect

ARTICLE Multi-Port PCB Jacks